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## **Prof. J. Joshua Yang**

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J. Joshua Yang is a professor of the Department of Electrical and Computer Engineering at the University of Southern California. He was a professor of the ECE department at the University of Massachusetts Amherst between Jan. 2015 and Aug. 2020. He spent about 8 years at HP Labs between 2007 and 2015, leading an emerging materials and devices team for memory and computing. His current research interest is Post-CMOS hardware for neuromorphic computing, machine learning and artificial intelligence, where he published a number of pioneering papers and holds 118 granted and about 60 pending US Patents. He was the chair of the 8th IEEE Nanotechnology SFBA Council Symposium on "Emerging Non-volatile Memory Technologies" and also the chair of the 10th symposium on "The Promise and Progress of Nanotech Enabled 2D Devices and Materials". He has guest-edited 7 journal special issues on Memory or unconventional computing. He serves on a number of Advisory Boards of international journals and conferences, such as ADVANCED INTELLIGENT SYSTEMS (Wiley), ADVANCED MATERIALS TECHNOLOGIES (Wiley), SMALL STRUCTURE (Wiley) and THE INTERNATIONAL CONFERENCE ON ELECTROCERAMICS etc. He was named as a Spotlight Scholar of UMass Amherst in 2017. He was also a recipient of UMass distinguished faculty lecturer and UMass Chancellor's Medal, the highest honor of UMass.